**CURRICULUM VITAE**

**MAHESH R K**

**ADDRESS:**

**#68, Sidda Totendra Nilaya**

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 **Objective**

To work in an organization where i can use my analytical and conceptual skills to contribute towards organization’s growth, which in turn will provide scope for my personal development.

 **Educational Qualification**

* Ph.d pursuing in Sharnbasva University Kalaburagi.
* M.Tech-VLSI Design & Embedded system-Dayanand Sagar College of Engineering (DSCE)

Bangalore-VTU-2011 -78%.

* B.E (E&CE)-Rural Engineering College Bhalki (BKIT), Bidar-VTU-2009 -61%.
* P.U.C- SVPST Ind. PU College Gulbarga-2005-63.33%.
* S. S. L. C- GOVT. High school Gulbarga-2003-73.44%.

**Technical Exposure**

* VHDL, Verilog and C languages.
* Technical aspects-CMOS VLSI, VLSI Design, Advanced Embedded system, Microprocessors, ARM , RTOS and Real Time systems.
* Hands on experience with Mentor Graphics tool,Matlab, Xilinx ISE,Labview,cadence,linux OS and keil software.

#### Professional Experience

 Designation: Asst. Professor Dept: Electronics and Communication

 College: APPA IET /Sharnbasva University Kalaburgi, **Experience**: 11.5 years

(18/09/2011 to till date)

**Subject Handled**: Logic design, electronic circuits, computer organization, Microprocessors,FCMOS VLSI,RTOS,Advanced Embedded system,CMOS RF Design, Digital system design using Verilog ,Advanced VLS systemI,VLSI Testing, ARM Cortex M3 & ES,SOC Design, Microcontroller and Programmable DSP,Real Time Operating Systems and Real time systems.

**Laboratories Handled:**

Analog Electronics lab, Logic design Lab, Microprocessor lab, Microcontroller Lab, Digital Signal Processing Lab, Advanced Embedded system Lab, Embedded controller lab,VLSI Lab,HDL Lab and MDSP Lab.

**Acadamic Projects Guided**

Guided Major projects on Embedded system, IOT, Image processing and VLSI for PG students-12 Members. And Guided Major projects on embedded system,VLSI and IOT for UG students-15 Groups.

 **Papers Presented in Conference/Published in Journal**

1. Mahesh R K and Prof.Ganesh M T “Invisible digital Image watermarking using spread spectrum Modulation Technique” 2nd **International conference** on communication, computation ,management and Nanotechnology(ICN-2011).
2. Mahesh R K and B.surykanth “A study of Planar inverted of antenna (PIFA) for wireless applications”2nd **National Conference** on Recent Innovations in Engineering and Technology (NCRIET-2015).
3. Rubina Firdous and Mahesh R K “Design and Implementation High Speed with Compensation Circuit Using KOGGE Stone Adder Tree” in **International Journal of Ethics in engineering and Management education**[ISSN:2348-4748], Volume2, Issue 5, May 2015, pp.17—20
4. Jyoti R Hallikhed and Mahesh R K “ VLSI Implementation of Fast Addition Using Quaternary Signed Digit Number System in **International Journal of Ethics in engineering and Management education**[ISSN:2348-4748, Volume2, Issue 5, May 2015, pp.49—53
5. Sadaf Rukshar and Mahesh R K “Design of A Multiband Flexible Divider Using A Low Power Single Phase Clock Distribution Network” in **International Journal of Ethics in Engineering & Management Education** [ISSN: 2348-4748], Volume 2, Issue 5, May 2015, pp.13-16.
6. Kubra Begum and Mahesh R K “Majority Logic Fault Detection with Difference Set Cyclic Code and LDPC Codes for Memory Applications” in **International Journal of in Engineering & Management Education** [ISSN: 2348-4748], Volume 2, Issue 5, May 2015, pp.28--31.
7. Manjunath and Mahesh R.K “Power Efficient Parallel Adder Design Using CNTFET Technology” ” in **International Journal of in Engineering & Management Education** [ISSN 2348-4748], Volume 2, Issue 4, April 2015, pp 28-31
8. Pooja G and Mahesh R K “Plant disease detection Using Image processing” in IJIRCCE,2nd **National Conference** on Recent Advances in Engineering & Tecnology(NCRAET\_2017)
9. Chndrakanth and Mahesh R K “Fire Detection system using Matlab” in **IJRASET**,volume 5, issue 7,July 2017.
10. Bhagyashree and Mahesh R K “A Review of Unified Automatic GAS Leakage Detector Using Arduino and GSM Module” **IJSRD - International Journal for Scientific Research & Development| Vol. 5, Issue 06, 2017 | ISSN (online): 2321-0613**
11. Anis Fatima and Mahesh R K “DESIGN OF HARDWARE RGB TO HMMD CONVERTER BASED ON REVERSIBLE LOGIC” **International Journal for Research in Applied Science & Engineering Technology (IJRASET)** *ISSN: 2321-9653; IC Value: 45.98; SJ Impact Factor:6.887 Volume 6 Issue VII, July 2018- Available at* [*www.ijraset.com*](http://www.ijraset.com)
12. Preeti B and Mahesh R k “Hybrid Power Genration System and Data Monitoring Through Web Page” **International Journal for Research in Applied Science & Engineering Technology (IJRASET)** *ISSN: 2321-9653; IC Value: 45.98; SJ Impact Factor: 7.177 Volume 7 Issue VI, June 2019- Available at* [*www.ijraset.com*](http://www.ijraset.com)
13. Roohi Farheen and Mahesh R K “WI-FI Acess point(WAP) Optimal placement in Indoor Location” Voloume 9,issue IX,September 2021 in **International Journal for Research in applied science and engineering technology(IJRASET).**
14. Mahesh R K and Shivkumar Jawaligi “Study of clustering in Vehicular Ad hoc Networks (VANETs) and Clustering-Based Efficient Data Dissemination Algorithms for VANET'S” in the **2nd International Conference on Futuristic trends in embedded system and Networking(ICFTEN 2021) organized by Dept.of ECE & IQAC RYMEC Bellary,Karnataka held between 7th and 8th July 2021**.
15. Mahesh R K and Shivkumar S jawaligi “Survey on cluster level routing with QoS in VANETs” **Turkish online journal of Qualitative Inquiry(TQJQI) Volume 12 Issue 6,July 2021:8455-8464.**
16. Mahesh R K,Shivkumar S Jawaligi “SFTD: A SMART forwarding technique based reliable data dissemination scheme for VANETs” **Measurements: Sensors Elsevier Publications. 2022, Volume 24, Pages 100572**
17. Sunita Reddy and Mahesh R K “Implementation of Segmentation based efficient imprecise multiplers with mux based fulladders” in the **IJSRST volume9,Issue4,July-August-2022**.

 **Participation in Webinars/Conferences**

1. Participated in the annual event X’PLORE-09 organised by IEEE-DSCE Students’ Branch and Participated in the **international conference**, ICSE 2010 held at between april 21-23,2010 at DSCE Bangalore.
2. Participated and attended 2 days **National level seminar** on Image processing and Communication systems(IPCS-2013) from 21st -22nd September 2013 organized by ECE,APPA IET GLB in association with IETE New Delhi and ISTE Bangalore chapter.
3. Participated in the 3 day **International conference** on “Recent trends in science,engineering,business studies,humanaties &social sciences” held during 30th August-1st September 2019,KLB
4. Participated in the **power seminar webinar** on the theme of “job skills to succeed in a post pandemic world” conducted by ICT academy on 4th may 2020.
5. Participated in the **power seminar webinar** on the theme of “Gearing up for the Job of Tomorrow” conducted by ICT academy on 5th may 2020
6. Participated in the **power seminar webinar** on the theme of “Employability skills for the future” conducted by ICT academy on 5th may 2020
7. Participated in one week **international webinar** series on “Power system Engineering & Electrical Vehicles” organized by EEE B.L.D.E.A’s V.P.Dr.P.G. Halakatti of Engg. & Technology, Vijaypura during 08/06/2020-12/06/2020.
8. Participated in **three day webinar** series on “Drone Technology” organized by Dept.of ECE,GSSS instititute of Engg.& technology for women ,mysuru Under IQAC,from 15th-17th june 2020.
9. Participated in **webinar** on “Doing Reserch:challenges,Dos and Don’ts” organized by Dept.of ECE,GSSS instititute of Engg.& technology for women,mysure Under IQAC cell on 16th June 2020.
10. Participated in **webinar** on “preparing for the Next phase of you carrier”, presented by Mr.kartikeyan Dhayalan,Practice Leader-Cyber Threat Response Services, Network security Delivery Team,IBM Singapore Pvt.Ltd held on 13th July organized and conducted by the Department of ISE,Rajarajeswari college of Engineering, Bangalore.
11. Participated in the **webinar** series on “Practical Analog & Mixed Signal Design” organized by IEEE circuits and systems(CAS) society Bangalore 0n 4th & 5th July 2020.
12. Participated in a Webinar on “Course Outcomes for Outcome Based Education(OBE)” organized by FET(Exclusively for Woman),Department of ECE on 9th July 2020.
13. Participated in the One-day National Level webinar on “ Recent Trends in Inverter Topology” organized by Department of EEE on 11 July 2020.
14. Participated in IP Awareness/Training program under National Intellectual property Awareness mission on January 11,2023

 **Participation in Workshops/Trainings/FDPs and Achievements**

1. Participated in the **Entrepreneurship camp** held at BKIT(REC) Bhalki from 28/04/2008-30/04/2008 conducted by Entrepreneurship development cell VTU,Belgaum.
2. Participated in the 10 days **campus recruitment training program** at BKIT(REC) Bhalki from 30/08/2008-08/09/2008 conducted by Universal Education Pvt.Ltd. Banglore.
3. Participated in the **workshop** on NS-2 from 22nd to 23rd June 2013 at APPA IET Gulbarga in association with ISTE, New Delhi.
4. Participated in the **FDP** on Oracle/SQL from 3rd to 6th August 2013 organized by CSE,APPA IET Gulbarga.
5. Participated and attended 2 days **National level workshop** on “Research Methodology” from 23rd -24th August 2014 organized by ECE,APPA IET GLB.
6. Participated and attended 2 days **Regional Level Workshop** on Current & Future Research Trends in Wired and Wireless Networks (CFRTWN-2014) from 6th -7th September 2014 organized by CSE,APPA IET GLB.
7. Participated in the One week state Level **FDP** on “Emerging Trends in Teaching, Learning and Research” from 4th t0 9th January 2016 at Appa institute of engineering and Technology kalaburagi.
8. TEQIP-III sponsored One week short term training program on “power Electronic and Drives” conducted by PG in PE,Department of EEE,PDCE Kalaburagi. From 5th -10th march 2018.
9. Participated in one week **FDP** on “Applications of LABVIEW in Teaching & Research” from 17th -22nd June 2019,at Dept. of ECE,FET, Sharnbasva University,KLB
10. Participated in the five days **workshop** on Hands on Training on Python Programming in association with SUK and Institution of engineers (India) kalabuagi local centre at Veerappa Nisty Engineering college Shorapur yadgir from 22nd to 26th oct 2019.
11. Participated in Two days **workshop** on “Awareness on intellectual property rights” during 12th-13th Dec 2019 in Association with KSCST IISC,Bengluru at Sharnbasva university,kalaburagi
12. Participated in five days Virtual **FDP** on “Next Generation Electronics and Computer Networks” held’s from 2nd-6th June 2020,organized by Dept.of ECE,BKIT bhalki.
13. Participated & completed successfully **ATAL Academy Online FDP** on “Internet of Things (IOT)” from 5/10/2020 to 9/10/2020 at SIT, KLB.
14. Participated & completed successfully **ATAL Academy Online FDP** on “Artificial intelligence” from 9/11/2020 to 13/11/2020 at PDCE, KLB.
15. Participated in the “Innovation for Green Future- Slogan Contest” and **secured 2nd Position**. The virtual event was conducted as part of World IPR Day Celebrations 2020.
16. Participated in a five day-day **online FDP** on “Current trends in Machine Learning with Python-A Hands-on Approach”-held from 6th-10th July 2020 organized by dept.of CSE,School of engineering, Dayanand sagar university Bangalore.
17. Participated in the **FDP** on “New Teaching Strategies and Techniques” held from 29th june-3rd July 2020,Organized by Reserch affairs Committee, under SOP cell,B.L.D.E.Association’s shree sanganbasva mahaswamiji polytechinique,vijaypur-03
18. Participated in 3day **FDP** on “Smart application Development Using IOT-Arduino organized by Dept.of ECE Vaageshwari college of Engineering,Karimnagar in Association with Pantech E-learning During 17/06/2020 to 19/06/2020.
19. Participated in **AICTE Training and Learning(ATAL)** Academy Online Elementary FDP on “Emerging Technological Challenges in Electric Vehicle” from 6th July 2021 to 11th July 2021 at Annapoorna Engineering college.
20. Participated in 5-day **FDP** on “Digital VLSI Design & Verification” Organized by Department of ECE,Bangalore Institute of Technology in Association with Entuple Technologies and **IEEE-BIT CAS** from 23rd to 27th August 2021.
21. Designing and Modelling of IOT,AI and ML Systems organized by **AICTE,ATAL Academy**,ARM Education and STM Microelectronics form August 1st to 5th 2022.
22. Completed 1-month Online internship on FPGA(30 days) at Pantech Prolabs india pvt Ltd Dec-Jan 2023

 **Hobbies**

* Reading magazines and books

 **Personal Profile**

Name : MAHESH

Father Name : RAJSHEKHAR KONKANALLI

**Date of Birth** : 01/06/1987

Languages Known: English, Kannada and Hindi

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 Post. Malkood-585317

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 **Phone No**.: 9538916432, 7892316959

**Declaration:**

I hereby declare that the above-mentioned information is correct up to best of my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars

**Place: Kalaburgi**

**Date: (MAHESH R K)**